

REMARKS

Reconsideration and allowance of the above-referenced application are respectfully requested.

Claims 1, 33-36, 54-58 and 91-95 stand rejected under 35 USC 103 as allegedly being obvious over Mawatari in view of Tanaka. This contention is respectfully traversed.

The rejection apparently asserts that the first and second conductive films 25, 26 of Tanaka correspond to the first and second plurality of conductive layers that are recited in the rejected claims and that Tanaka's conductive layers could be used to modify Mawatari. However, Tanaka's conductive layers are completely different than the claimed layers. Tanaka column 9, lines 28-45, it explains that the first and second conductive films are simultaneously formed when forming scanning electrodes or signal electrodes. Thus, these first and second conductive films 25, 26 are formed on the same surface. This is shown in Fig. 1.

In contrast, the rejected claims require an insulating film (the "second insulating film") between the first and second plurality of conductive layers. Tanaka clearly could not do this in view of the above. Therefore even if Mawatari were modified to include Tanaka's first and second conductive layers,

the combination would not have an insulating film between the layers and would hence be different from the claimed structure.

In addition, the rejected claims require the first conductive layers to be formed of a same material as the plurality of scanning lines; and also require the second plurality of conductive layers to be formed of a same material as the plurality of signal lines. Tanaka fails to teach this combination. Although Tanaka teaches first and second conductive films that are associated with the scanning electrodes on one substrate, and first and second conductive films 25, 26 are associated with signal electrodes. This is very different than what is claimed. The rejected claims require that both the first and second plurality of conductive layers are formed between the first substrate and the sealing member. This is not taught or suggested by Mawatari in view of Tanaka. Therefore, for these reasons, it is respectfully suggested that all of these claims should be in condition for allowance.

Applicants also respectfully request that the Examiner officially consider and cite the documents which were listed on the PTO-892 that was submitted with this filing. Applicants also file an Information Disclosure Statement including additional documents.

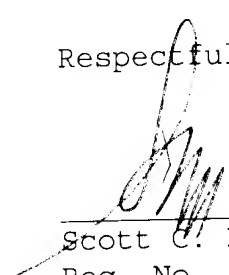
In view of the above amendments and remarks, therefore, all of the claims should be in condition for allowance. A formal notice to that effect is respectfully solicited.

Attached is a marked-up version of the changes being made by the current amendment.

Enclosed is a \$110 check for the Petition for Extension of Time fee. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

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Version with markings to show changes made

In the claims:

Claim 1 has been amended as follows:

1. (Amended) A display device comprising:
 - a first substrate;
 - a plurality of scanning lines extending over the first substrate in a first direction;
 - a plurality of signal lines extending over the first substrate in a second direction,
 - a plurality of thin film transistors disposed at each intersection of said scanning lines and said signal lines;
 - a plurality of pixel electrodes electrically connected to said thin film transistors;
 - a first insulating film disposed between said scanning lines and said signal lines;
 - a second substrate opposed to the first substrate;
 - a sealing member disposed at a periphery of said first and second substrates;
 - a first plurality of conductive layers interposed between said first substrate and said sealing member, said first plurality of conductive layers comprising a same material as said plurality of scanning lines;
 - a second plurality of conductive layers interposed between said first substrate and said sealing member, said second plurality of conductive layers comprising a same material as said plurality of signal lines; and
 - a second insulating film disposed between said first plurality of conductive layers and said second plurality of

conductive layers wherein said second insulating film comprises a same material as said [interlayer] first insulating film,

wherein said first plurality of conductive layers and said second plurality of conductive layers are arranged in turn so that said second plurality of conductive layers do not overlap said first plurality of conductive layers.